

Curriculum vitae

Thijs Krol was born on November 17, 1942, in Leeuwarden, The Netherlands. He studied electrical engineering at Eindhoven University of Technology, The Netherlands, from which he graduated cum laude on January 14, 1971. In 1971 he joined Philips Research Laboratories. From 1971 to 1985 he has been working in different areas of fault-tolerant computing, error-correcting codes and telecommunication switching systems. Since 1985 he is mainly involved with specification and high-level hardware description languages for VLSI design.

From 1983 to 1990 he has been guest-lecturer in fault-tolerant computing at the Eindhoven University of Technology.